

ABSTRACT OF THE DISCLOSURE

A stable charge coupled device (CCD) imaging apparatus operable in multiple frame rates for displaying a signal having a low frame rate in a viewfinder (VF), and also a recorder built-in type imaging apparatus using the CCD imaging apparatus are presented. The imaging apparatus includes a drive pulse switching circuit for multiplying a CCD drive pulse other than a CCD read pulse by $(n/2)$ (where n is an arbitrary integer) when the multi-frame rate is low, a frame memory for storing an output signal of the CCD of one frame right after pulse output. The signal is read at every $(n/2)$ frames. The frame memory repeats to read out the stored signal in one frame $(n/2)$ times. The imaging apparatus further includes a camera signal processing circuit for performing a camera process to an output signal of the frame memory, and a recorder unit for recording an output signal of the camera signal processing circuit at the frame rate of the set mode. As a result, even at a low frame rate, the apparatus displays a signal in the VF. And with a properly-selected value of n , the apparatus conducts a signal having various frame rate between a camera and the recorder at a common frame rate.